ROC920010009US1 1/3 NODE B 10 14 -12 000 Р1 18 P2 12 16-- 16 -000 NODE C P1 FIG. 1 P1 PN P2 P2 00 NODE A NODE D PN P1 PN 🗀 P2 12 000 NODE E □000□ ☐ PN NODE F 0 0 12 I/O REGISTERS NODE A MEMORY INT. CONFIGURATION REGISTER PROCESSOR(S) HANDLER CRS COMMUNICATION REGISTER 1 COMMUNICATION REGISTER 2 32 22 20 25 - 30 46 ō COMMUNICATION REGISTER N 26 -I/O HUB CONTROL ŏ I/O REGISTERS ŏ CIRCUIT 0 PN Р1 NODE REGISTER ADDRESS SPACE 000

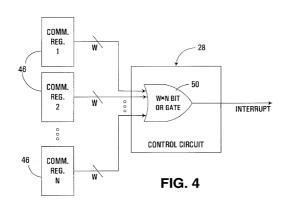
- 18

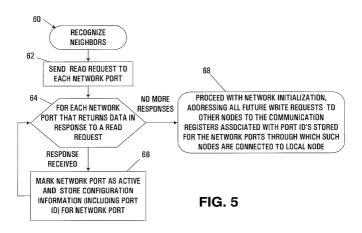
FIG. 2

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FIG. 3

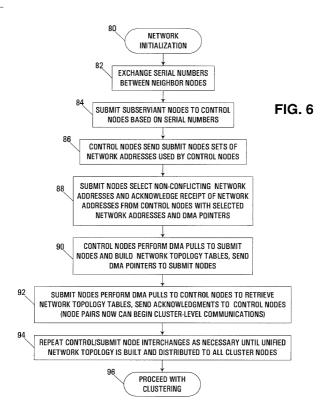
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